

HC11/Micro11 Interrupt Summary (1 of 2)

NAME	ACTIVATED BY	VECTOR	EFFECT ON CCR	CCR/(LOCAL) MASK	MICRO11 VECTOR
$\overline{\text{RESET}}$	Low then high on $\overline{\text{Reset}}$ or Power-on	FFFE:FFFF	1->S, 1->X, 1->I	None	7FFE:7FFF (Note 1)
Clock Monitor	E clock too slow	FFFC:FFFD	1->S, 1->X, 1->I	(CME bit in OPTION)	7FFC:7FFD
COP Watchdog	Watchdog time-out	FFFA:FFFB	1->S, 1->X, 1->I	(NOCOP bit in CONFIG)	7FFA:7FFB
Illegal Opcode	Undefined opcode	FFF8:FFF9	1->X, 1->I	None	7FF8:7FF9
Software Interrupt	SWI instruction	FFF6:FFF7	1->X, 1->I	None	7FF6:7FF7
$\overline{\text{XIRQ}}$	Low level on $\overline{\text{XIRQ}}$	FFF4:FFF5	1->X, 1->I	X bit	7FF4:7FF5
$\overline{\text{IRQ}}$	Low level or NGE on $\overline{\text{IRQ}}$	FFF2:FFF3	1->I	I bit	7FF2:7FF3
Real Time Int.	RTI timer expires	FFF0:FFF1	1->I	I bit (RTI in TMSK2)	7FF0:7FF1
Input Capture 1	PGE, NGE or both on IC1/PA2	FFEE:FFEF	1->I	I bit (IC1I in TMSK1)	7FEE:7FEF
Input Capture 2	PGE, NGE or both on IC2/PA1	FFEC:FFED	1->I	I bit (IC2I in TMSK1)	7FEC:7FED
Input Capture 3	PGE, NGE or both on IC3/PA0	FFEA:FFEB	1->I	I bit (IC3I in TMSK1)	7FEA:7FEB

NOTES

1. If this vector is 0x1040, execution from RESET will be directed to 0x1040. Otherwise Micro11 runs (0xC000). Allows user program to run within 64~.

HC11/Micro11 Interrupt Summary (2 of 2)

NAME	ACTIVATED BY	VECTOR	EFFECT ON CCR	CCR/(LOCAL) MASK	MICRO11 VECTOR
Output Compare 1	TOC1 = TCNT	FFE8:FFE9	1->I	I bit (0C1I in TMSK1)	7FE8:7FE9
Output Compare 2	TOC2 = TCNT	FFE6:FFE7	1->I	I bit (0C2I in TMSK1)	7FE6:7FE7
Output Compare 3	TOC3 = TCNT	FFE4:FFE5	1->I	I bit (0C3I in TMSK1)	7FE4:7FE5
Output Compare 4	TOC4 = TCNT	FFE2:FFE3	1->I	I bit (0C4I in TMSK1)	7FE2:7FE3
Output Compare 5	TOC5 = TCNT	FFE0:FFE1	1->I	I bit (0C5I in TMSK1)	7FE0:7FE1
Timer Overflow	TCNT overflow	FFDE:FFDF	1->I	I bit (TOI in TMSK2)	7FDE:7FDF
Pulse Acc OverFI	PACNT Overflow	FFDC:FFDD	1->I	I bit (PAOVI in TMSK2)	7FDC:7FDD
Pulse Acc Edge	PGE or NGE on PAI	FFDA:FFDB	1->I	I bit (PAII in TMSK2)	7FDA:7FDB
Serial Peripheral Interface	SPI xfer complete	FFD8:FFD9	1->I	I bit (SPIE in SPCR)	7FD8:7FD9
SCI Interrupts	RDRF=1, TDRE=1, TC=1, IDLE=1 or OR=1 (Note 2)	FFD6:FFD7	1->I	I bit (RIE, TIE, TCIE and ILIE in SCCR2)	7FD6:7FD7

NOTES

- If multiple SCI interrupt sources are enabled, then the individual source of the interrupt must be determined by interrogating the SCSR for RDRF (char received), TDRE (char transmitted), TC (transmit complete), IDLE (idle line wake-up) or OR (overrun error) as applicable.